

Department of Electronics & Communication Engineering

ABESIT/ECE/2021-22/Notice/001

Date: 01st Sep 2021

Academic Calendar, Odd Semester: 2021-22

S. No	Event	Date(s)	Day	Responsibilities	
1	Commencement of classes B.Tech. (IV Year Students)	01-09-2021	Wednesday		
2	Commencement of classes B.Tech. (III Year Students)	13-09-2021	Monday		
3	Class Test-I	27-09-2021 to 29-09-2021	Monday to Wednesday	Mr. Vaibhav Jain	
4	Mentor-Mentee Meeting – I	30-09-2021	Thursday		
5	Final Year Project Progress Presentation	04-10-2021 to 06-10-2021	Monday to Wednesday	Mr. Rachit Patel/ Dr. H.M. Gaur	
6	Class Test-II	18-10-2021 to 20-10-2021	Monday to Wednesday	Mr. Vaibhav Jain	
7	Sessional-I	B.Tech III & IV year	25-10-2021 to 30-10-2021	Monday to Saturday	50% of syllabus & each paper of 3 hours duration in line with Dr. APJAKTU theory examination papers
8	Hands-on Session on “ Introduction to Microcontroller & IoT ”	Oct 2021		Mr. Aviral Malay	
9	Students Feedback - I	08-11-2021	Monday		
10	Mentor-Mentee Meeting-II	08-11-2021	Monday		
11	Final Year Project Progress Presentation	15-11-2021 & 17-11-2021	Monday to Wednesday	Mr. Rachit Patel/ Dr. H.M. Gaur	
12	Expert Talk cum Workshop on “ SOC Design & Verification ”	Nov 2021		Resource Person: Mr. S.K. Sahoo, Sr. Lead Verification, ST Microelectronics Coordinators: Dr. H.M. Gaur & Mr. Rachit Patel	
13	Class Test-III	29-11-2021 to 01-12-2021	Monday to Wednesday	Mr. Vaibhav Jain	
14	Workshop on “ Presentation & Report Writing ”	Dec 2021		Dr. H.M. Gaur	
15	Mentor Mentee Meeting –III	13-12-2021	Monday		
16	Students Feedback - II	13-12-2021	Monday		
17	Final Year Project Progress Presentation	13-12-2021 & 15-12-2021	Monday to Wednesday	Mr. Rachit Patel/ Dr. H.M. Gaur	
18	Sessional-II	B.Tech II,III,IV year	18-12-2021 to 24-12-2021	Saturday to Friday	100% of syllabus & each paper of 3 hours duration in line with Dr. APJAKTU theory examination papers
19	End Semester Examination*			As per University schedule	

*Subject to University Schedule



Dr. Hari Mohan Gaur
Associate Professor & Head
Department of Electronics & Communication Engineering

CC to the following for information & necessary action:

1. Prof.(Dr.) M.K.Jha- Director (email)
2. Prof.(Dr.) Sapna Katiyar, Dean Academics (email)
3. Dr. H.M. Gaur, Head ECE (email)
4. Head of Departments (email)
5. All faculty members, ECE (email)
6. Dr. Rakesh Singh Tomar, Head-Library(email)
7. Prof. Basant Pandey, Registrar (email)
8. Record Room.

CC: For information:

1. Hon'ble Chairman Sir (email)