



# ABESIT

## Department of Electronics & Communication Engineering (Resume of Teaching Faculty)

### (1) Personal Details:

Name : Anchal Tyagi  
 Gender : Female  
 Date of Birth : 19-10-1993  
 Father's Name : Mr. Jaipal Tyagi  
 Mother's Name : Mrs. Asha Tyagi  
 Marital Status : Married  
 Mobile No. : 9958739828  
 E-mail ID : anchal.tyagi@abesit.in  
 Date of Joining : 01-07-2017  
 Designation : Assistant Professor

### Photograph



### (2) Educational Qualification:

Doctorate Degree	University/Institution	Year
Ph.D.	JIIT, NOIDA	Pursuing
Thesis/Dissertation Title	Gait Recognition	

Highest Degree	University/Institution	Year	Marks (%)	Div	Subject
M. Tech	JIIT, NOIDA	2016	85%	I	Communication System

### (3) Experience Research/Industrial Experience Teaching:

Total Experience :1.8 years

### (4) Number of FDP/Workshop/Orientation Program/Short Term Course/Refresher Course:

S. No.	FDP/Workshop/Orientation Program/Short Term Course/Refresher Course
1	Attended FDP on "Artificial Intelligence & Machine Learning: Interdisciplinary Approach for Applications" from 12th -16th June, 2018 at ABESIT, Ghaziabad.
2	Attended FDP on "Signal Processing and its Applications" from 23rd -28th April, 2018 at JSSATE, Noida.
3	Attended FDP on "Business Intelligence& Artificial Intelligence" on 9 Jan., 2018 at ABESIT, Ghaziabad.
4	Attended a National Seminar on "Advanced Power Electronics & Motor Control Simulation Techniques" on 7th October, 2017 at ABESIT, Ghaziabad.
5	Attended FDP on "Analog & Embedded Systems" from 17th -22nd July, 2017 at ABESIT, Ghaziabad.

### (6)Coordinator/Co-Coordinator/Core Member of Different Activities:

1. Member of Fine Arts Committee in Festin 'O' Beat 9.0.1.
2. Head coordinator of girl's Table Tennis in Zonal Sports Fest 2017-18.